

Announcement – **236509** – Advanced topics in Hardware Security, **Tuesday 10:30 – 13:30 Taub 5**

Prof. Avi Mendelson - avi.mendelson@technion.ac.il

will focus this semester on **“Hands on Hardware Accelerators for Machine learning”**

The course will be given in English if required

The course will focus of the reciprocal relations between programming styles, machine learning algorithms and the hardware accelerators that supports them. The course will cover (partial list)

- CPU based accelerators
- GPU based accelerators
- Support for low power
- Binarization and quantization
- FPGA and ASIC accelerators

Prerequisites

- A course in machine learning
- A course in Computer Architecture (or equivalent)

Grade:

- 30% - 3-4 exercises
- 30% - Presentation of a paper in the field (20-30 minutes)
- 40% - Final work – handle (individuals)

For more information please send an email to avi.mendelson@technion.ac.il